

Fig. 1
(Prior Art)

Fig. 2 (Prior Art)

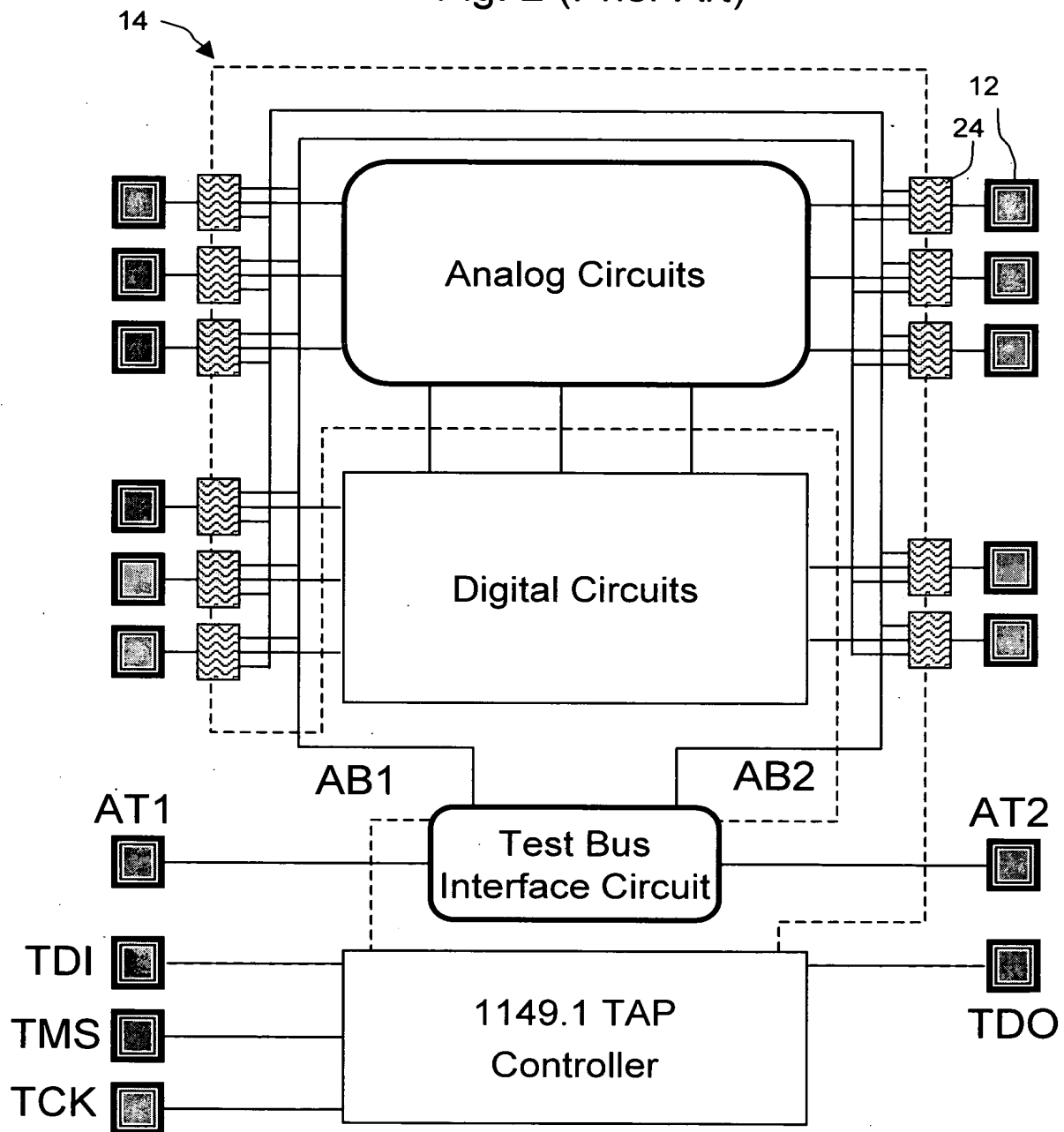


Fig. 3
(Prior Art)

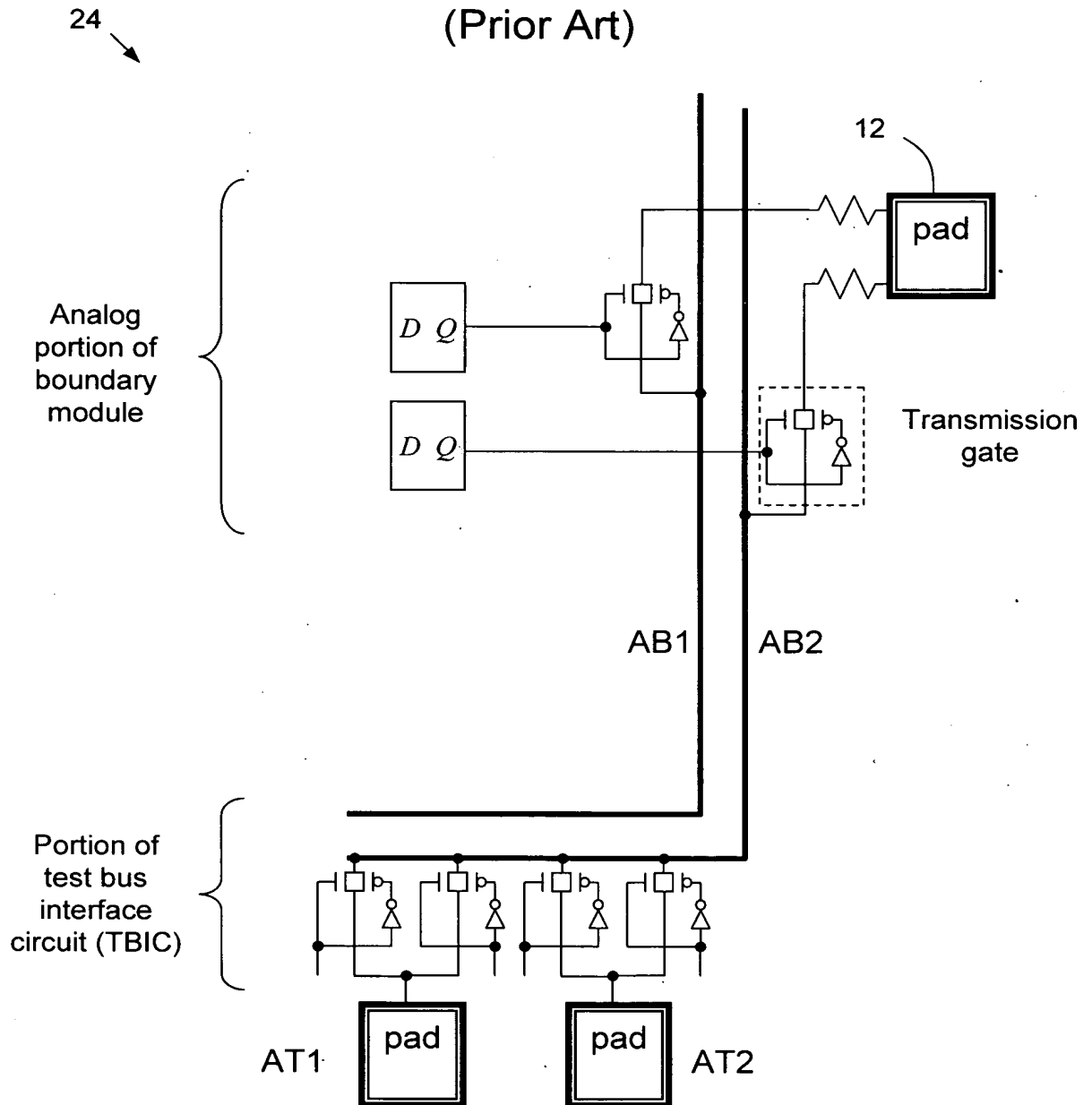


Fig. 4A Prior Art)

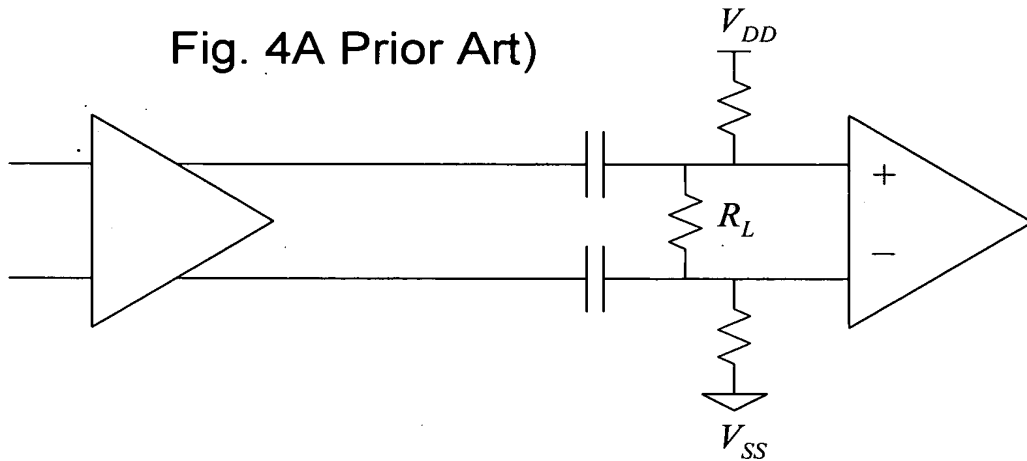


Fig. 4B (Prior Art)

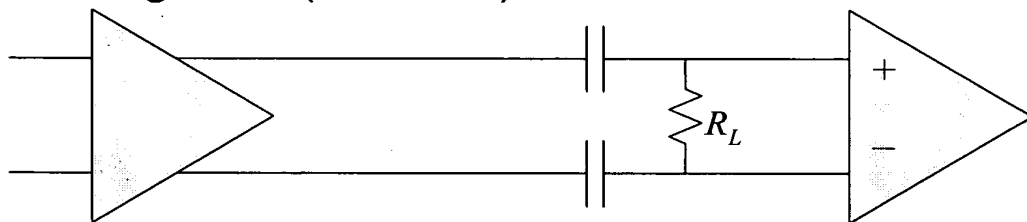


Fig. 4C (Prior Art)

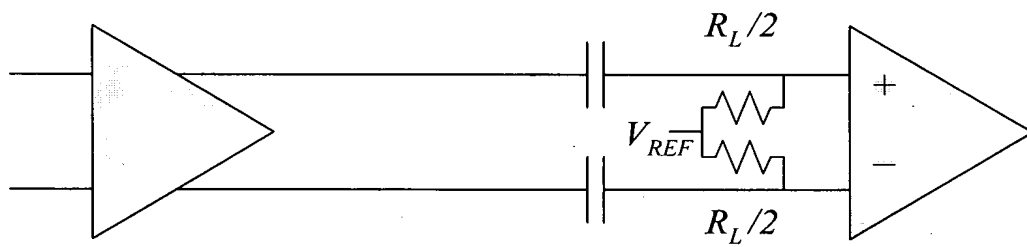
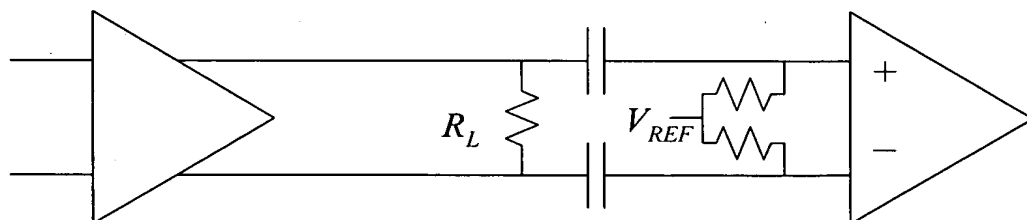


Fig. 4D (Prior Art)



Data dependent jitter caused by a simple RC low pass filter

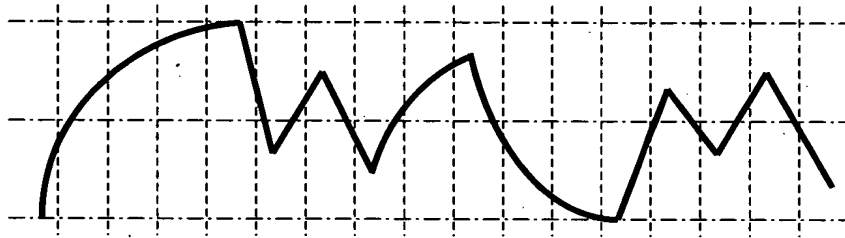


Fig. 5
(Prior Art)

Fig. 6A

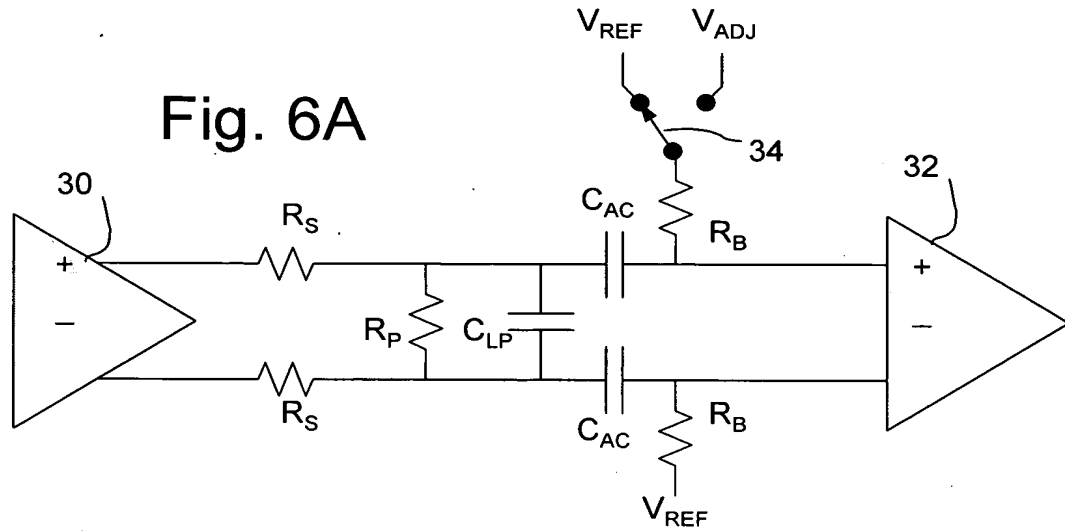


Fig. 6B

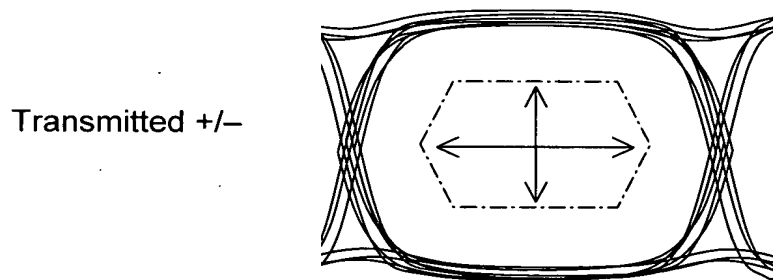


Fig. 6C

V_{ADJ} equal to V_{REF}

Received +/-

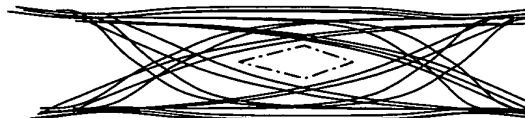


Fig. 6D

V_{ADJ} not equal to V_{REF}

Received +
Received -

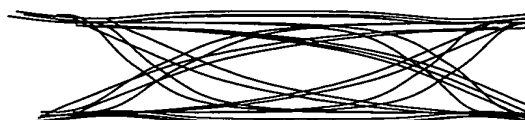


Fig. 6E



Fig. 6F



Fig. 6G

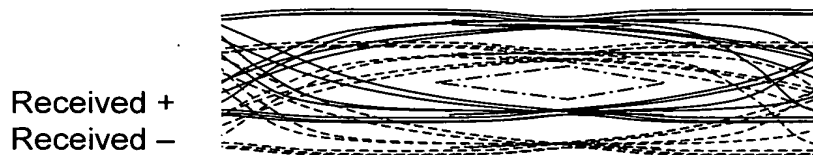


Fig. 7A

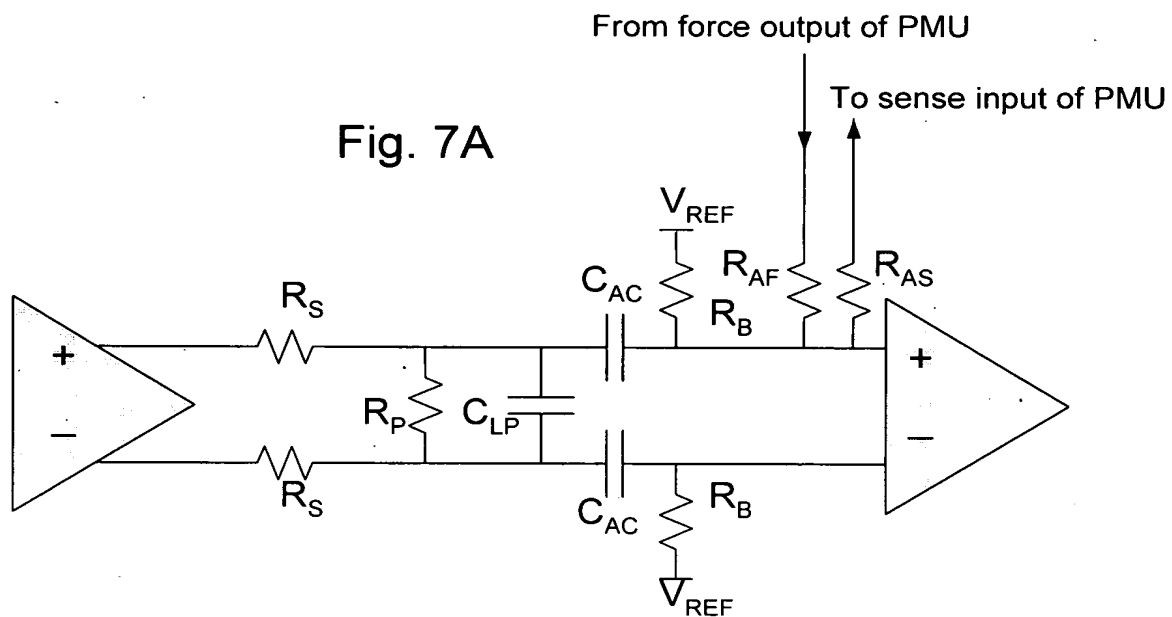
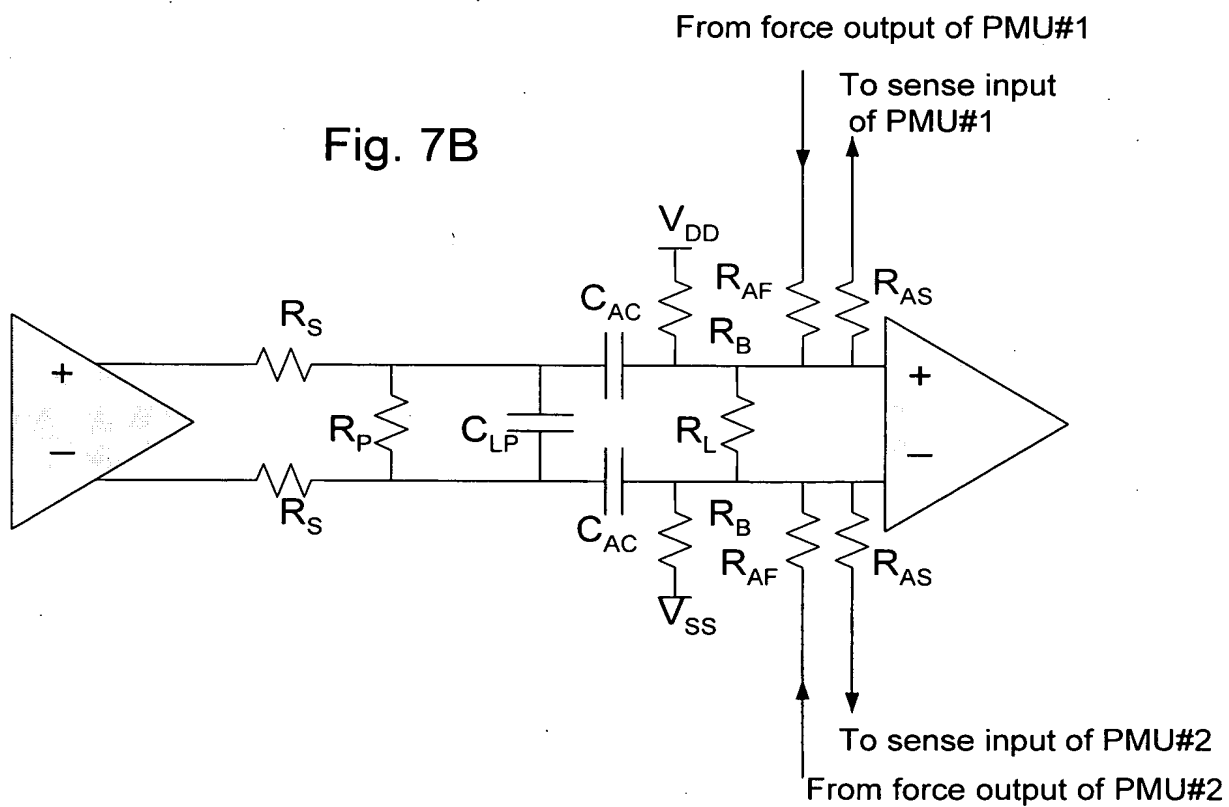


Fig. 7B



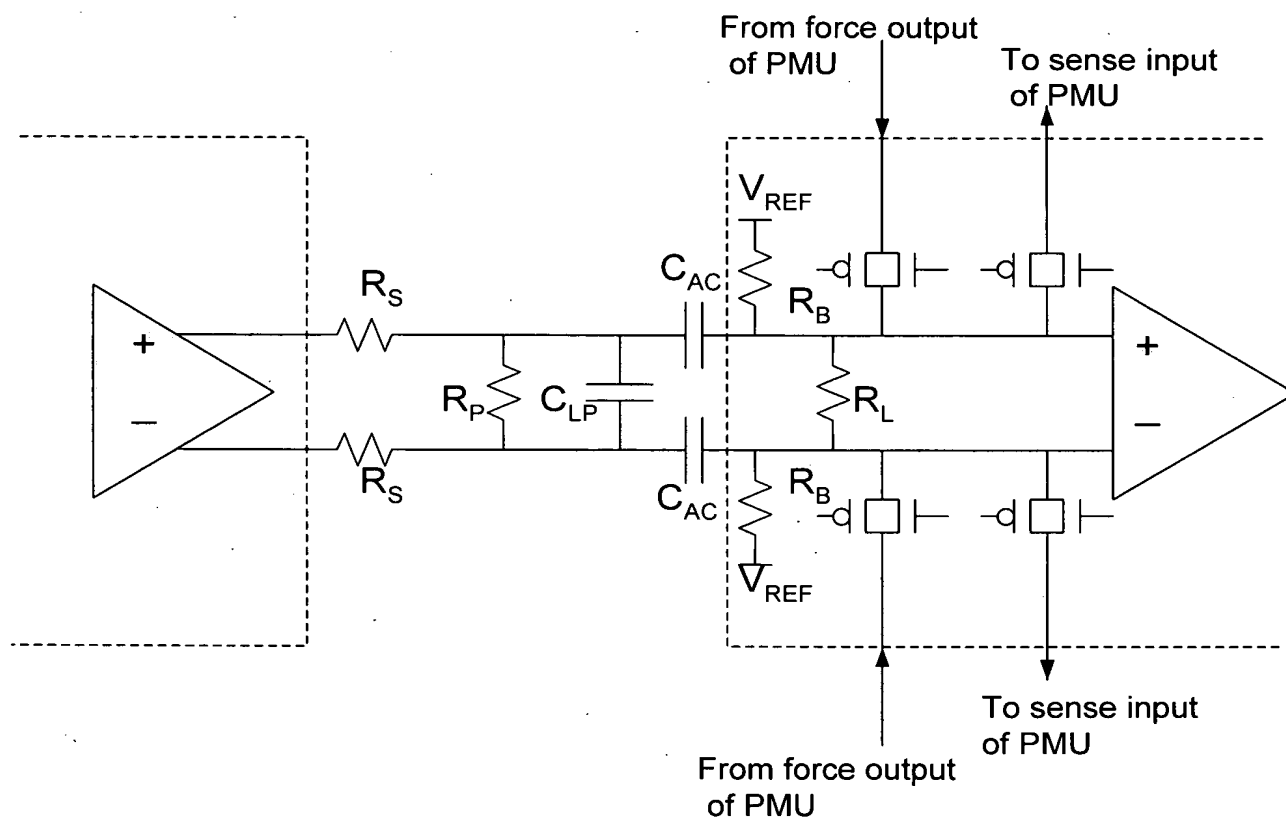


Fig. 9

